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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/756,624	01/14/2004	Akira Ohta	402946	2510

23548 7590 06/09/2005

LEYDIG VOIT & MAYER, LTD
700 THIRTEENTH ST. NW
SUITE 300
WASHINGTON, DC 20005-3960

EXAMINER

TAKAOKA, DEAN O

ART UNIT	PAPER NUMBER
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2817

DATE MAILED: 06/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/756,624

Applicant(s)

OHTA ET AL.

Examiner

Dean O. Takaoka

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 5 is/are allowed.
- 6) ☒ Claim(s) 1, 2 and 4 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 January 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>1/14/04</u> . | 6) <input type="checkbox"/> Other: ____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 2 and 4 are rejected under 35 U.S.C. 102(b) as being anticipated by Ishida et al. (U.S. Patent No. 6,201,439).

Claim 1:

Ishida et al. (best shown in Figs. 1 and 7) shows a high-frequency power amplifier (Fig. 1) comprising a multilayer substrate including a plurality of laminated layers (Fig. 7); a wiring prohibited area located on at least one of an obverse (2170; e.g. facing) and a reverse side (2210) of the multilayer substrate in which wiring is prohibited (where 2170 and 2210 are ground areas where ground areas are wiring prohibited areas); a first strip conductor (2060) located within the multilayer substrate; a second strip conductor (2070) located at a position in a lamination direction of the multilayer substrate (e.g. vertical stacked layers) different from that of the first strip conductor; a via (2130) electrically connecting the first and second strip conductors; and first (2170) and second (2210) grounding conductors disposed sequentially in the lamination direction of the multilayer substrate and sandwiching the first and second strip conductors, where at least one of the first and second ground conductors includes a first grounding conductor portion for the wiring prohibited area and a second grounding

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conductor portion for an area other than the wiring prohibited area so that the second grounding conductor is positioned in the lamination direction of the multilayer substrate different from the first grounding conductor (where 2170 and 2200 are different, thus inherently positioned differently).

Claim 2:

A first wiring prohibited area located on the obverse side of the multilayer substrate (2170) and a second wiring prohibited area located on the reverse side of the multilayer substrate (2210), the second wiring prohibited area overlapping the first wiring prohibited area when viewed in the lamination direction of the multilayer substrate, where length of the first wiring prohibited area in the lamination direction of the multilayer substrate is different than the second wiring prohibited area (where 2170 and 2210 are different lengths from a top view, e.g. vertically stacked lamination).

Claim 4:

Where one end of the first grounding conductor runs along a periphery of the via when viewed in the lamination direction (where the edges of ground 2170 run parallel to via 2130 when viewed from the top).

Allowable Subject Matter

Claim 5 is allowed.

Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Hase et al. – shows a high frequency module.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dean O. Takaoka whose telephone number is (571) 272-1772. The examiner can normally be reached on 8:30a - 5:00p Mon - Fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



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June 6, 2005